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By: 0 P.

Date: 8/6/01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant

Lars-Peter Heineck et al.

Applic. No.

09/885,553

Filed

June 20, 2001

Title

MOS Transistor in a Single-Transistor Memory Cell Having

a Locally Thickened Gate Oxide

Art Unit

2814

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks, Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States Patent No. 5,382,533 (Ahmad et al.), dated January 17, 1995;

United States Patent No. 5,679,968 (Smayling et al.), dated October 21, 1997;

United States Patent No. 5,714,413 (Brigham et al.), dated February 3, 1998;

European Patent Application EP 0 621 632 A1 (Rajeevakumar et al.), dated October 26, 1994.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications,

patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,

For Applicants

Date: August 6, 2001

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AUG.	FORM TO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))				Sheet 1 of 1 Attorney Docket No.: Applic. No. GR 98 P 1379 D 09/885,553				
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	U.S. PATENT DOCUMENTS								
	EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILI DA	
	Y	Α	5,382,533	01/17/95		 		<u> </u>	
	19/1	В	5,679,968	10/21/97	Smayling et al.				
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	OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)								
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